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Bib Data Sheet

CONFIRMATION NO. 7845

SERIAL NUMBER 10/608,883	FILING DATE 06/26/2003 RULE	CLASS 438 257	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. F0361.C1.D1
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APPLICANTS

Steven C. Avanzino, Cupertino, CA;

Pin-Chin Connie Wang, Menlo Park, CA;
Minh Van Ngo, Fremont, CA;

** CONTINUING DATA ***** *S.W.S.*
This application is a DIV of 10/226,520 08/22/2002 PAT 6,642,145
which is a CON of 09/874,175 06/04/2001 PAT 6,469,385

** FOREIGN APPLICATIONS ***** *None S.W.S.*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 09/15/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 2	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance Verified and Acknowledged <i>S.W.S.</i> Examiner's Signature Initials				

ADDRESS

22898
THE LAW OFFICES OF MIKIO ISHIMARU
1110 SUNNYVALE-SARATOGA ROAD
SUITE A1
SUNNYVALE, CA
94087

TITLE

Integrated circuit with dielectric diffusion barrier layer formed between interconnects and interlayer dielectric layers

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